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AMENDMENT

Chuong D. Ngo

PATENT

TOWNSEND and TOWNSEND and CREE LLF

In re application of:

Application No.: 08/993,442

Filed: December 18, 1997

For: VIDEO FRAME RENDERING

Earle W. Jennings, III et al.

ENGINE

Box RCE

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the final office action mailed June 24, 2002, please amend this application as follows:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner:

AMENDMENT

IN THE CLAIMS:

Please amend claims 1 and 10 as indicated. Please add new claims 21-24.

1

4

(Amended) An integrated circuit for image frame rendering and

2 DSP applications, the integrated circuit during operation operating with memory, the

3 integrated circuit comprising:

an interface circuit configured to control access to said memory, the

5 interface circuit coupled to said memory;